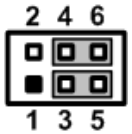
Jumper Settings

Before applying power to AX93291, please make sure all jumper(s) are in factory default positions.

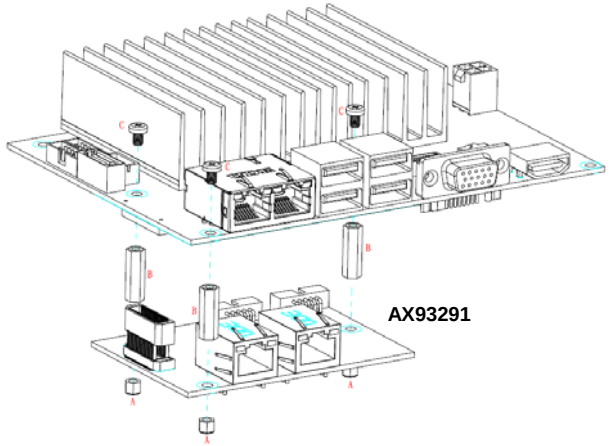
Jumper	Description		Setting
JP1	COM1 Data/Power Selection Default: RS-232 Data	CN1 Pin 1: DCD	3-5 Close
		CN1 Pin 8: RI	4-6 Close

COM1 Data/Power Selection (JP1)
The COM1 port (CN1) has +5V level power capability on DCD and +12V level on RI by setting this jumper.

Description	Setting
Power: Set CN1 pin 1 to +5V level	1-3 close
Data: Set CN1 pin 1 to DCD (Default)	3-5 close
Power: Set CN1 pin 8 to +12V level	2-4 close
Data: Set CN1 pin 8 to RI (Default)	4-6 close



Assembly Drawing



Gently insert ZIO module into CPU board's ZIO connector, see figure above. Align assembly holes and use screws to firmly secure ZIO module to CPU board. Be careful not to over-tighten the screws.

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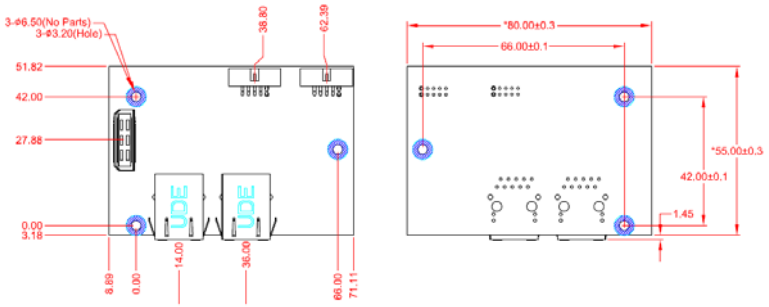
AX93291 Quick Installation Guide

Checklist

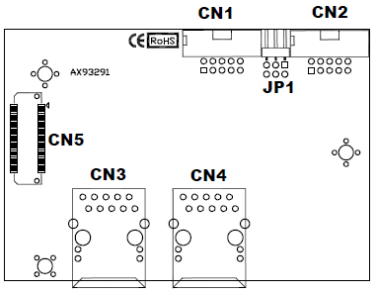
- ✓ I/O board x1
- ✓ Quick installation guide x1
- ✓ Screw pack x1
- ✓ Com cable x2

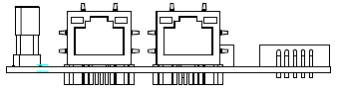
Note: ***Please contact your local vendors if any damaged or missing items. DO NOT apply power to the module if there is any damaged component.***

Dimension and Fixing Holes



Module Layout





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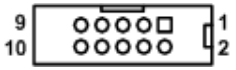
Connectors

Connector	Description
CN1	COM1 Connector
CN2	COM2 Connector
CN3	Ethernet Port 1
CN4	Ethernet Port 2
CN5	ZIO Expansion Connector

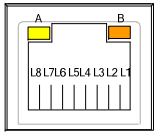
COM Connectors (CN1 and CN2)
This module comes with two Molex 78046-1020, 2x5-pin p=2.00mm box headers; CN1 (for COM1) and CN2 (for COM2). Only CN1 is equipped with power capability on DCD and RI pins by setting JP1.

Pin	Signal	Pin	Signal
1	DCD	2	DSR
3	RXD	4	RTS
5	TXD	6	CTS
7	DTR	8	RI
9	GND	10	N.C.



Ethernet Ports (CN3 and CN4)
This module has two RJ-45 connectors: CN3 and CN4. Ethernet connection can be established by plugging one end of the Ethernet cable into this RJ-45 connector and the other end (phone jack) to a 1000/100/10-Base-T hub.

Pin	Signal	Pin	Signal
L1	MDI0+	L5	MDI2+
L2	MDI0-	L6	MDI2-
L3	MDI1+	L7	MDI3+
L4	MDI1-	L8	MDI3-
A	Active LED (Yellow)		
B	1000 LAN LED (Orange) / 100 LAN LED (Green)		



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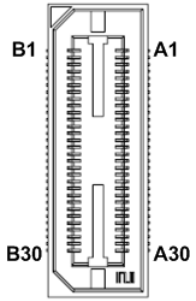
ZIO Expansion Connector (CN5)
The CN5 is for connecting ZIO expansion module to CPU board such as CAPA840.

Pin	Signal	Pin	Signal
A1	+12V	B1	GND
A2	+5VSB	B2	GND
A3	+5VSB	B3	GND
A4	+3.3VSB	B4	GND
A5	+3.3VSB	B5	RSVD
A6	RSVD	B6	RSVD
A7	RSVD	B7	RSVD
A8	SMB_CLK	B8	RSVD
A9	SMB_DATA	B9	GND
A10	GND	B10	PCI-E TXP4
A11	PCI-E RXP4	B11	PCI-E TXN4
A12	PCI-E RXN4	B12	WAKE_N
A13	RSVD	B13	PCI-E CLKP
A14	RSVD	B14	PCI-E CLKN
A15	RSVD	B15	GND
A16	No Use	B16	CLK_33M
A17	No Use	B17	SERIRQ
A18	GND	B18	LAD0
A19	No Use	B19	LAD1
A20	No Use	B20	LAD2
A21	PLTRST_N	B21	LAD3
A22	No Use	B22	L_FRAME
A23	No Use	B23	GND
A24	GND	B24	No Use
A25	No Use	B25	No Use
A26	No Use	B26	No Use
A27	No Use	B27	No Use
A28	No Use	B28	GND
A29	GND	B29	No Use
A30	No Use	B30	No Use



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